



**TECHNISCHE  
UNIVERSITÄT  
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Faculty of Computer Science Institute of Systems Architecture, Operating Systems Group

# TRUSTED COMPUTING

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**Goal:** Understand principles of:

- Authenticated booting, relation to (closed) secure booting
- Remote attestation
- Sealed memory
- Dynamic root of trust, late launch
- Protection of applications from the OS
- Point to implementation variants (TPM, SGX, TrustZone)

## **Non-Goal:**

- Deep discussion of cryptography
- Lots of details on TPM, TCG, TrustZone, SGX, ...
  - Read the documents once needed

- Secure Booting
- Measured / authenticated Booting
- (Remote) Attestation
- Sealed Memory
- Late Launch / dynamic root of trust
- Trusted Computing (Group)
- Trusted Computing Base
- **Beware of terminology chaos!**



## **Trusted Computing Base (TCB):**

- Set of all components (*hardware, software, procedures*) that must be relied upon to enforce a security policy

## **Trusted Computing (Technology):**

- Particular technology, often comprised of authenticated booting, remote attestation, and sealed memory

## **Trusted Computing Group (TCG):**

- Consortium behind a specific trusted computing standard

# Key Goals of Trusted Computing

- Prevent certain software from running
- Which computer system do I communicate with?
- Which stack of software is running ...
  - ... in front of me?
  - ... on my server somewhere?
- Restrict access to certain secrets to certain software
- Protect an application from the OS

## Digital Rights Management (DRM):

- Vendor sells content
- Vendor creates key, encrypts content with it
- Client downloads encrypted content, stores it locally
- Vendor sends key, but wants to ensure that only specific software can use it
- Has to work also when client is offline
- **Vendor does not trust the client**



## Virtual machine by cloud provider:

- Client rents compute and storage (server / container / virtual machine)
- Client provides its own operating system (OS)
- Needs to ensure that provided OS runs
- Needs to ensure that provider cannot access data
- **Customer does not trust cloud provider**



## Industrial Plant Control:

- Remote operator sends commands, keys, ...
- Local technicians occasionally run maintenance / selftest software, install software updates, ...
- **Local technicians are not trusted**

## **Anonymity Service:**

- Provides anonymous communication over internet (e.g., one node in mix cascade)
- Law enforcement can request introduction of surveillance functionality (software change)
- **Anonymity-service provider not trusted**

## **Measuring:**

- Process of obtaining metrics of platform characteristics
- Example: Hash code of software

## **Attestation:**

- Vouching for accuracy of (measured) information

## **Sealed Memory:**

- Binding information to a (software) configuration



## Hash: $H(M)$

- Collision-resistant hash function  $H$  applied to content  $M$

## Asymmetric key pair: $E_{\text{pair}}$ consisting of $E_{\text{priv}}$ and $E_{\text{pub}}$

- Asymmetric private/public key pair of entity  $E$ , used to either conceal (encrypt) or sign some content
- $E_{\text{pub}}$  can be published,  $E_{\text{priv}}$  must be kept secret

## Symmetric key: $E$

- Symmetric key of entity  $E$ , must be kept secret (“secret key”)

## Digital Signature: $\{M\}E_{\text{priv}}$

- $E_{\text{pub}}$  can be used to verify that **E** has signed **M**
- $E_{\text{pub}}$  is needed and sufficient to check signature

## Concealed Message: $\{M\}E_{\text{pub}}$

- Message **M** concealed (encrypted) for **E**
- $E_{\text{priv}}$  is needed to unconceal (decrypt) **M**

**Example:** program vendor FooSoft (FS)

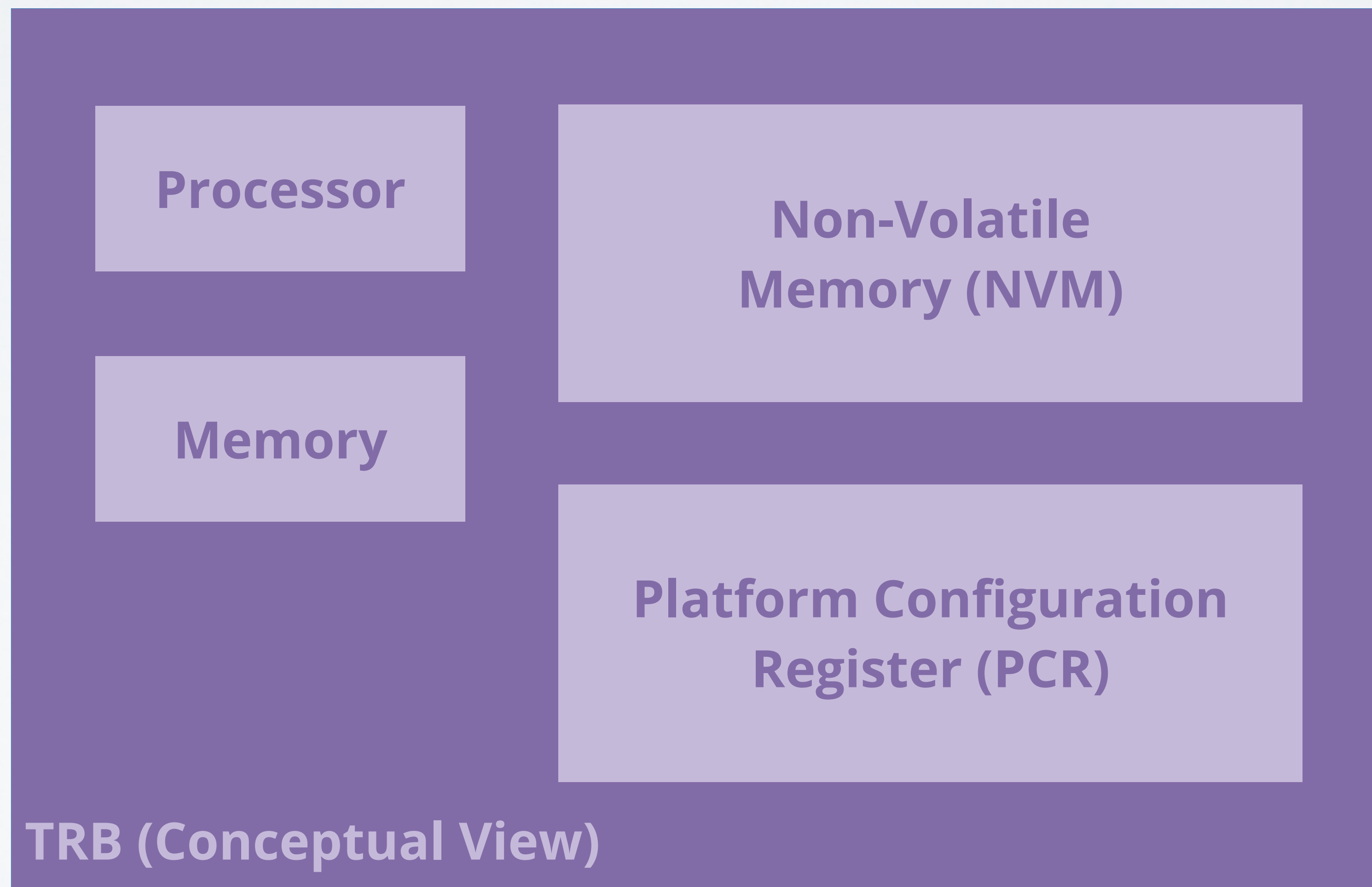
Software identity **ID** must be known

**Two ways to identify software:**

- By hash:  **$ID_{\text{Program}} = H(\text{Program})$**
- By signature:  **$\{\text{Program}, ID_{\text{Program}}\}FS_{\text{priv}}$** 
  - Signature must be available (e.g., shipped with program)
  - Use  **$FS_{\text{pub}}$**  to check signature
  - **$(H(\text{Program}), FS_{\text{pub}})$**  can serve as  **$ID_{\text{Program}}$**



# Tamper-Resistant Black Box (TRB)



## OS stored in read-only memory (flash)

### Hash $H(OS)$ in TRB NVM, preset by manufacturer:

- Load OS code, compare  $H(\text{loaded OS code})$  to preset  $H(OS)$
- Abort if different

### Public key $FS_{pub}$ in TRB NVM, preset by manufacturer:

- Load OS code, check signature of loaded OS code using  $FS_{pub}$
- Abort if check fails

## Steps:

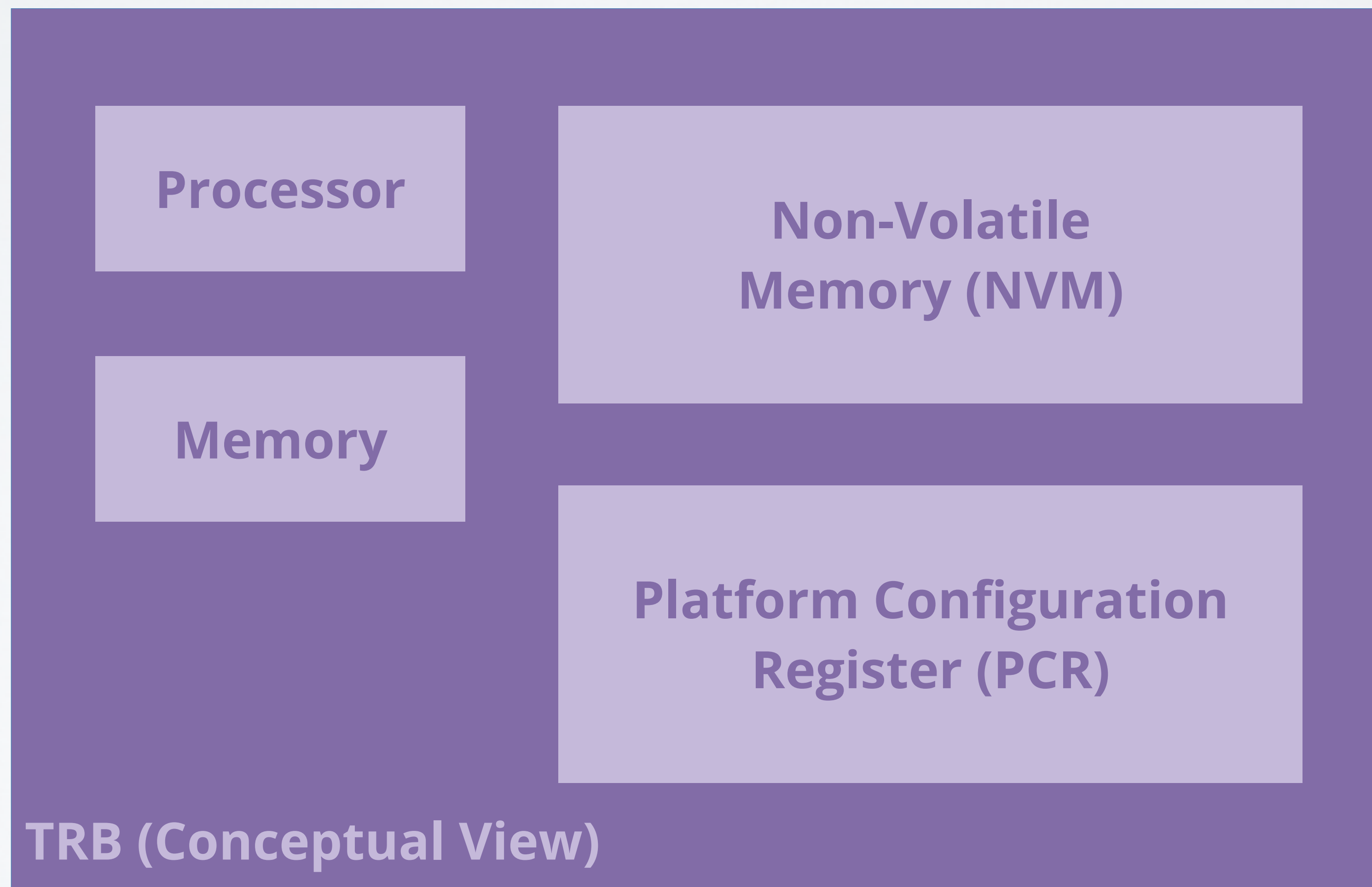
- 1) Preparation by OS and TRB vendors
- 2) Booting & measuring
- 3) Remote attestation



## 1a) Preparation by OS vendor:

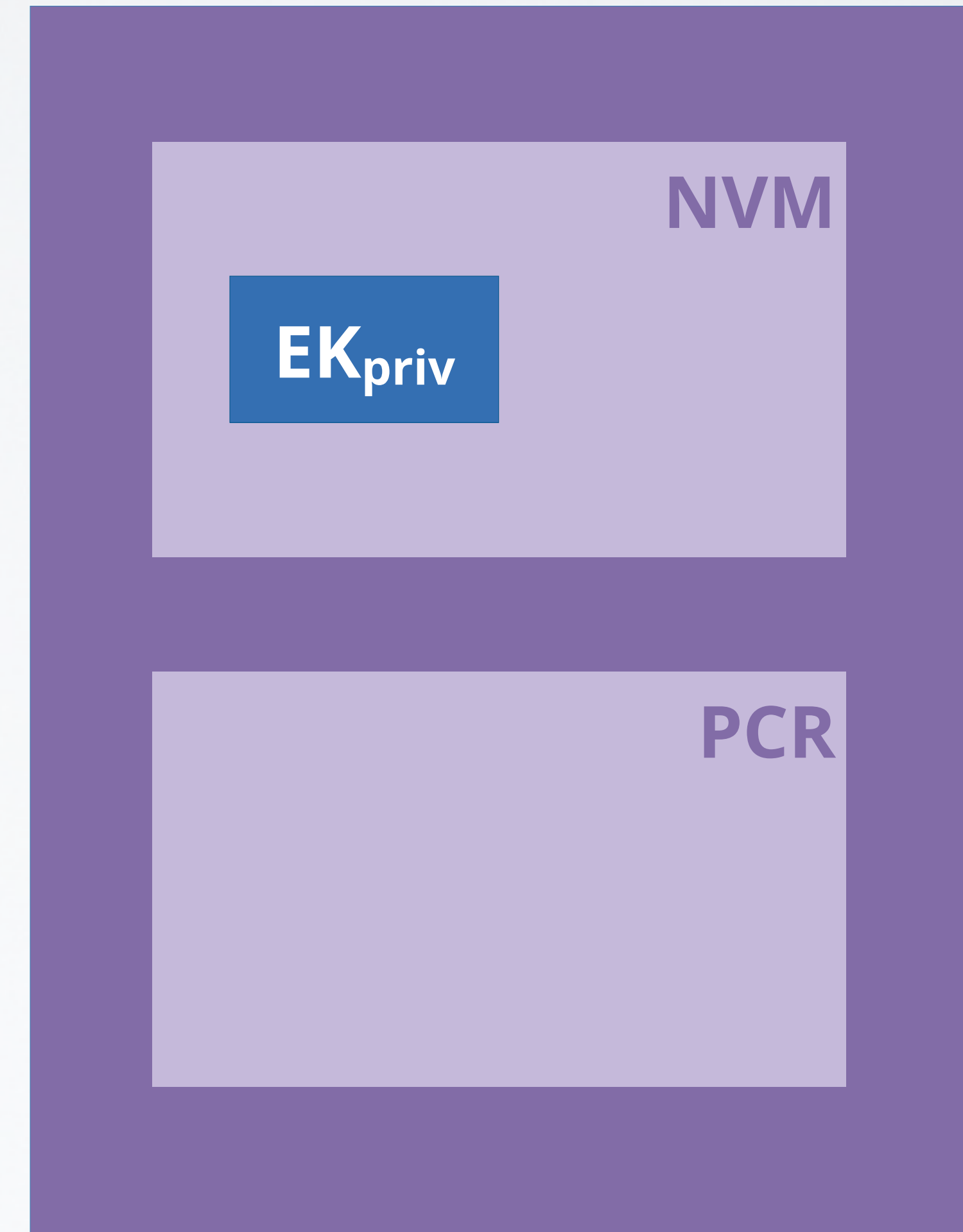
- Certifies: {„a valid OS“, **H(OS)**}**OSVendor**<sub>priv</sub>
- Publishes identifiers: **OSVendor**<sub>pub</sub> and **H(OS)**

# Tamper-Resistant Black Box (TRB)



## 1b) Preparation by TRB vendor:

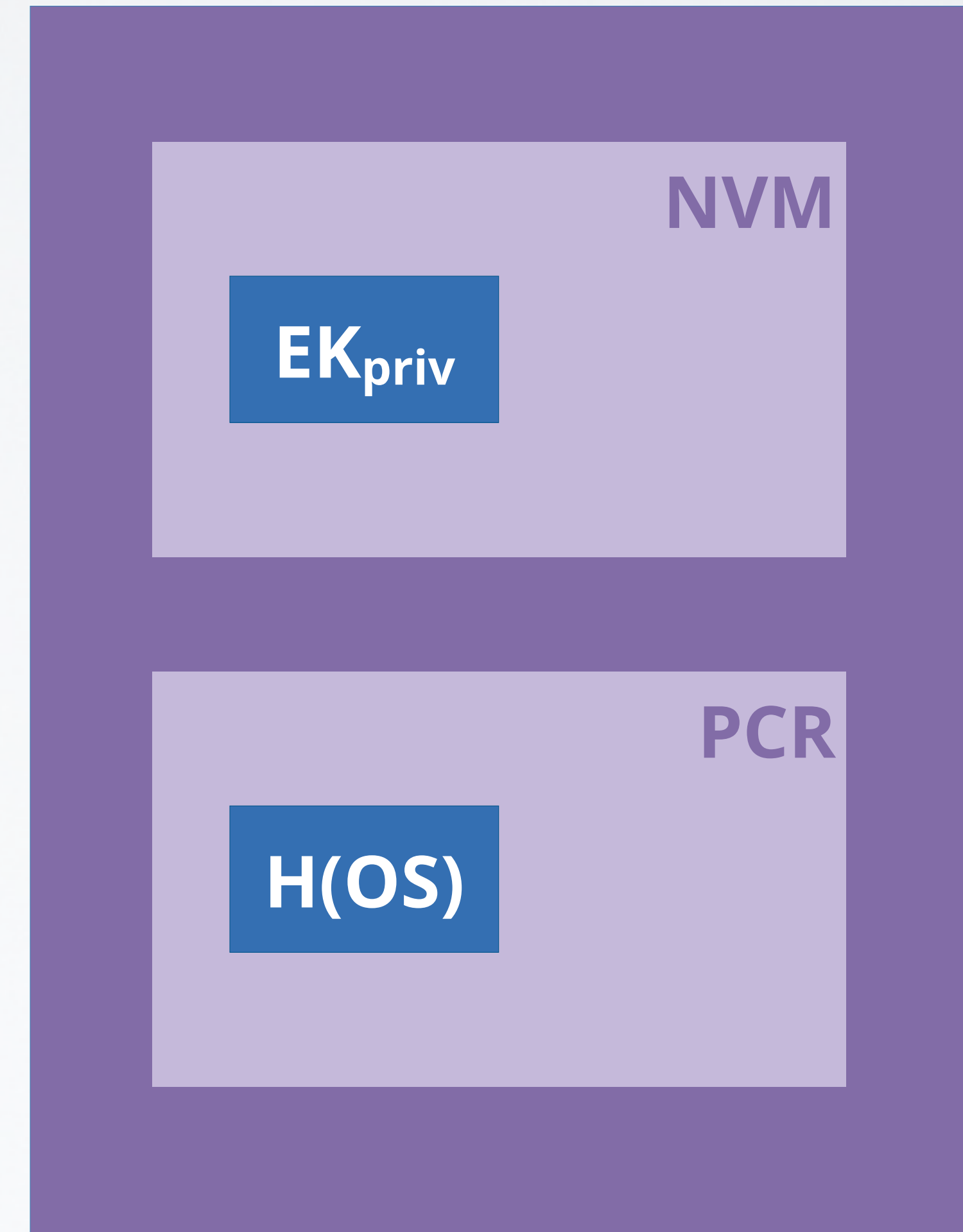
- TRB generates "Endorsement Key" pair: **EK<sub>pair</sub>**
- TRB Stores **EK<sub>priv</sub>** in TRB NVM
- TRB publishes **EK<sub>pub</sub>**
- TRB vendor certifies:  
{ "a valid EK", **EK<sub>pub</sub>** }  
**TRBVendor<sub>priv</sub>**





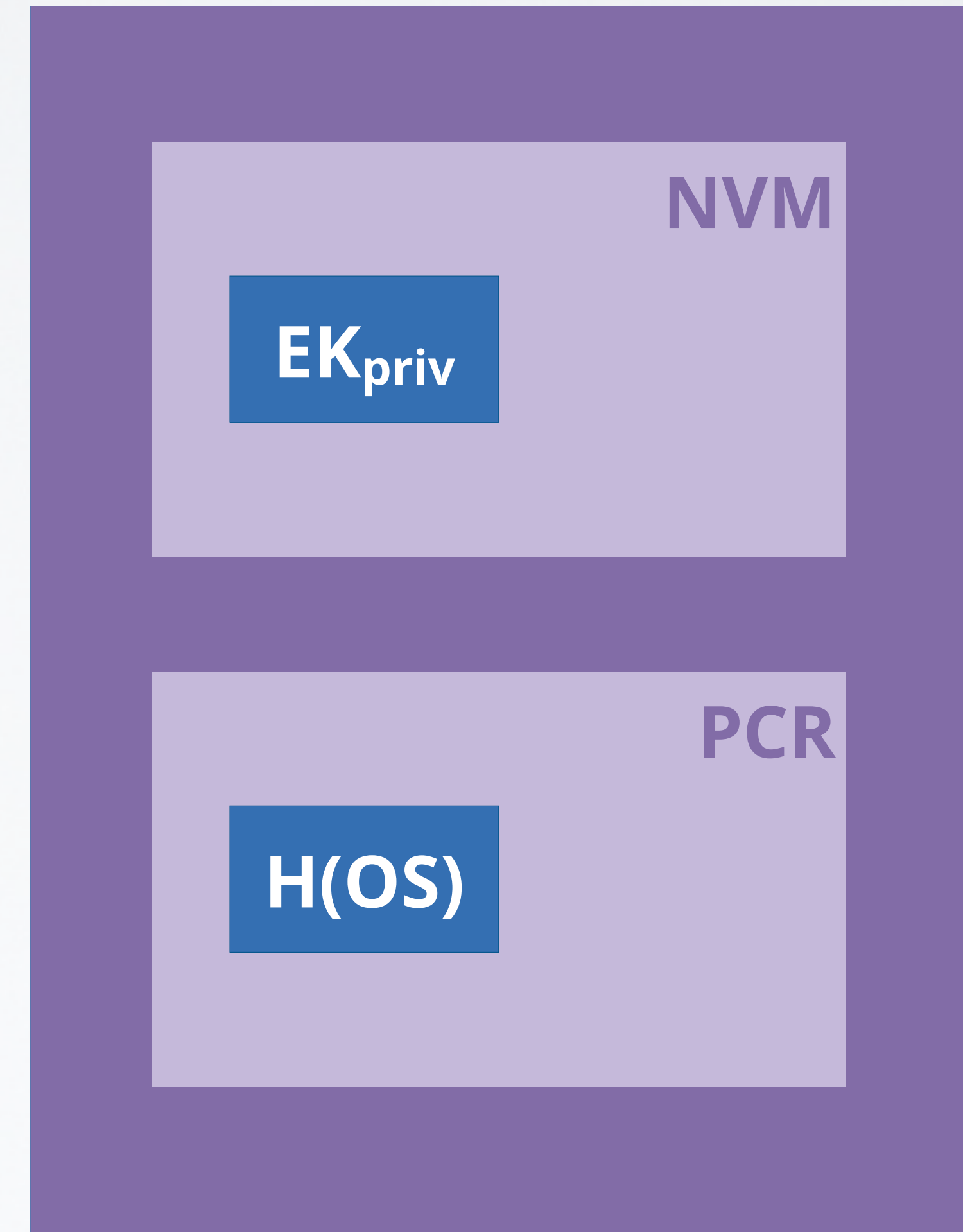
## 2) Booting & measuring:

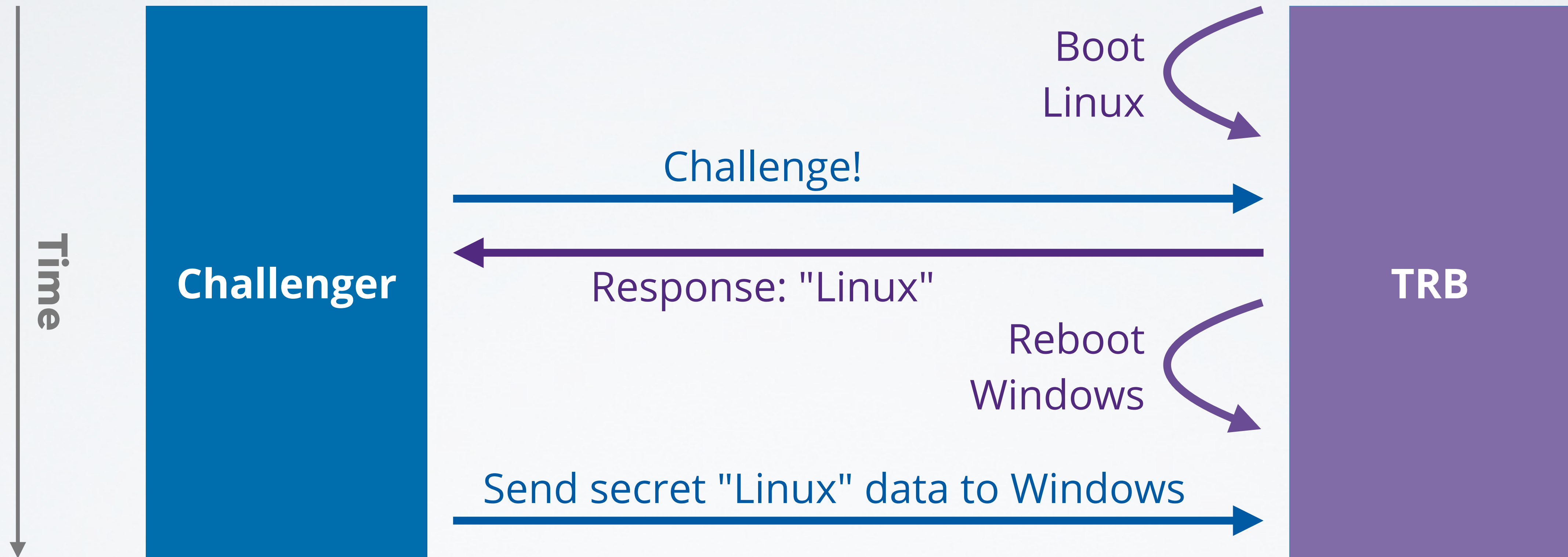
- TRB resets
- TRB computes ("measures") hash **H(OS)** of loaded OS
- Records **H(OS)** in platform configuration register **PCR**
- **Note: PCR** not directly writable, more on this later



## 3) Remote Attestation:

- Remote computer sends "challenge": **NONCE**
- TRB signs  $\{\mathbf{NONCE}, \mathbf{PCR}\}E\mathbf{K}_{\text{priv}}$  and sends it to "challenger"
- Challenger checks signature, decides if OS identified by **H(OS)** in reported signed **PCR** is OK





**Problem:** Time-of-check, time-of-use (TOCTOU) attack possible

**Solution:** Create new key pair for protecting data until next reboot



## At each boot, TRB does the following:

- Computes **H(OS)** and records it in **PCR**
- Creates two key pairs for the booted, currently active OS:
  - **ActiveOSAuthK<sub>pair</sub>** /\* for authentication (signing) \*/
  - **ActiveOSConK<sub>pair</sub>** /\* for concealing (encryption) \*/
- TRB certifies:  
**{ActiveOSAuthK<sub>pub</sub>, ActiveOSConK<sub>pub</sub>, H(OS)}EK<sub>priv</sub>**

## Remote Attestation:

- Challenger sends: **NONCE**
- Currently booted, active OS generates response:  
 **$\{\text{ActiveOSCon}K_{\text{pub}}, \text{ActiveOSAuth}K_{\text{pub}}, H(\text{OS})\}EK_{\text{priv}}$**   
 **$\{\text{NONCE}\}ActiveOSAuthK_{\text{priv}}$**

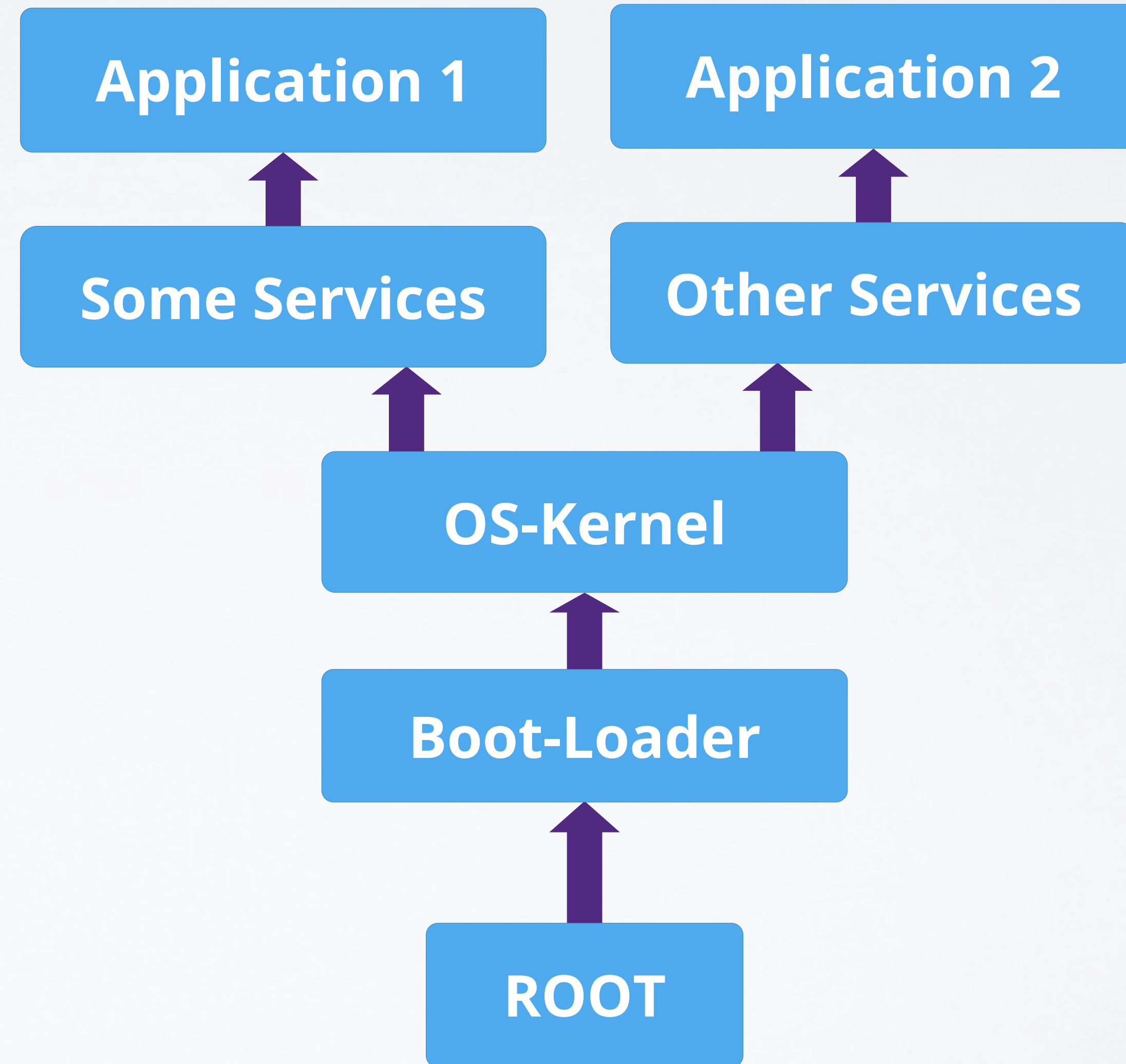
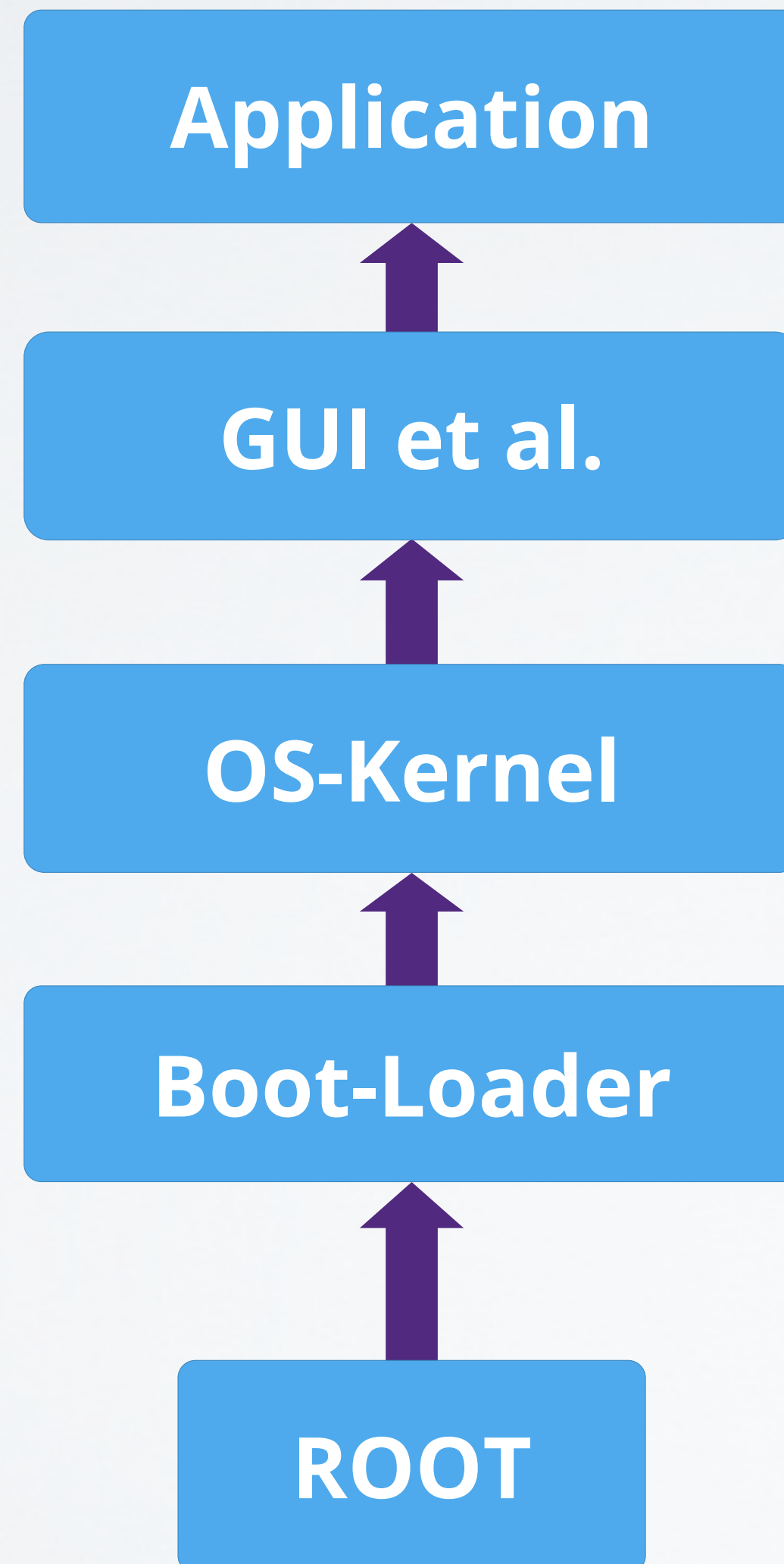
## Client sends data over secure channel:

- **$\{\text{data for active OS}\}ActiveOSConK_{\text{pub}}$**

**Authenticated booting** and **remote attestation** as presented are secure, if:

- 1) TRB can protect **EK<sub>priv</sub>**, **PCR**
- 2) OS can protect "Active OS" keys
- 3) Rebooting destroys content of:
  - PCR
  - "Active OS keys" in memory





## Two Concerns:

- Very large Trusted Computing Base (TCB) for booting (including device drivers, etc.)
- Remote attestation of one process (leaf in tree)

## Extend operation:

$$\mathbf{PCR}_n = \mathbf{H}(\mathbf{PCR}_{n-1} \parallel \mathbf{new\ component}) \quad [\mathbf{PCR}_0=0]$$

## Software Stack:

- 1 PCR value  $\mathbf{PCR}_n$  after  $\mathbf{n}$  components have been measured

## Software "Tree":

- 1 PCR value  $\mathbf{PCR}_n$  for each leaf at end of a branch of length  $\mathbf{n}$
- Needs multiple PCRs (1 per branch) that share state from **Root** to  $\mathbf{PCR}_0s$ , then diverge to leafs at  $\mathbf{PCR}_{App1}$ ,  $\mathbf{PCR}_{App}$ , ...



## Key pairs per level of tree:

- OS controls applications → generate additional key pair per application
- OS certifies:
  - **{Application 1, App1K<sub>pub</sub>}ActiveOSAuth<sub>priv</sub>**
  - **{Application 2, App2K<sub>pub</sub>}ActiveOSAuth<sub>priv</sub>**

**Problem:** huge software to boot system

**Solution:** late launch

- Use arbitrary software to start system and load all software
- Provide specific instruction to enter "secure mode"
  - Put hardware in secure state (stop all processors, I/O, ...)
  - Measure software and record into PCR
- **AMD (skinit):** hashes arbitrary "secure loader" and start it
- **Intel (senter):** starts boot code (must be signed by Intel)

## Use case from earlier example:

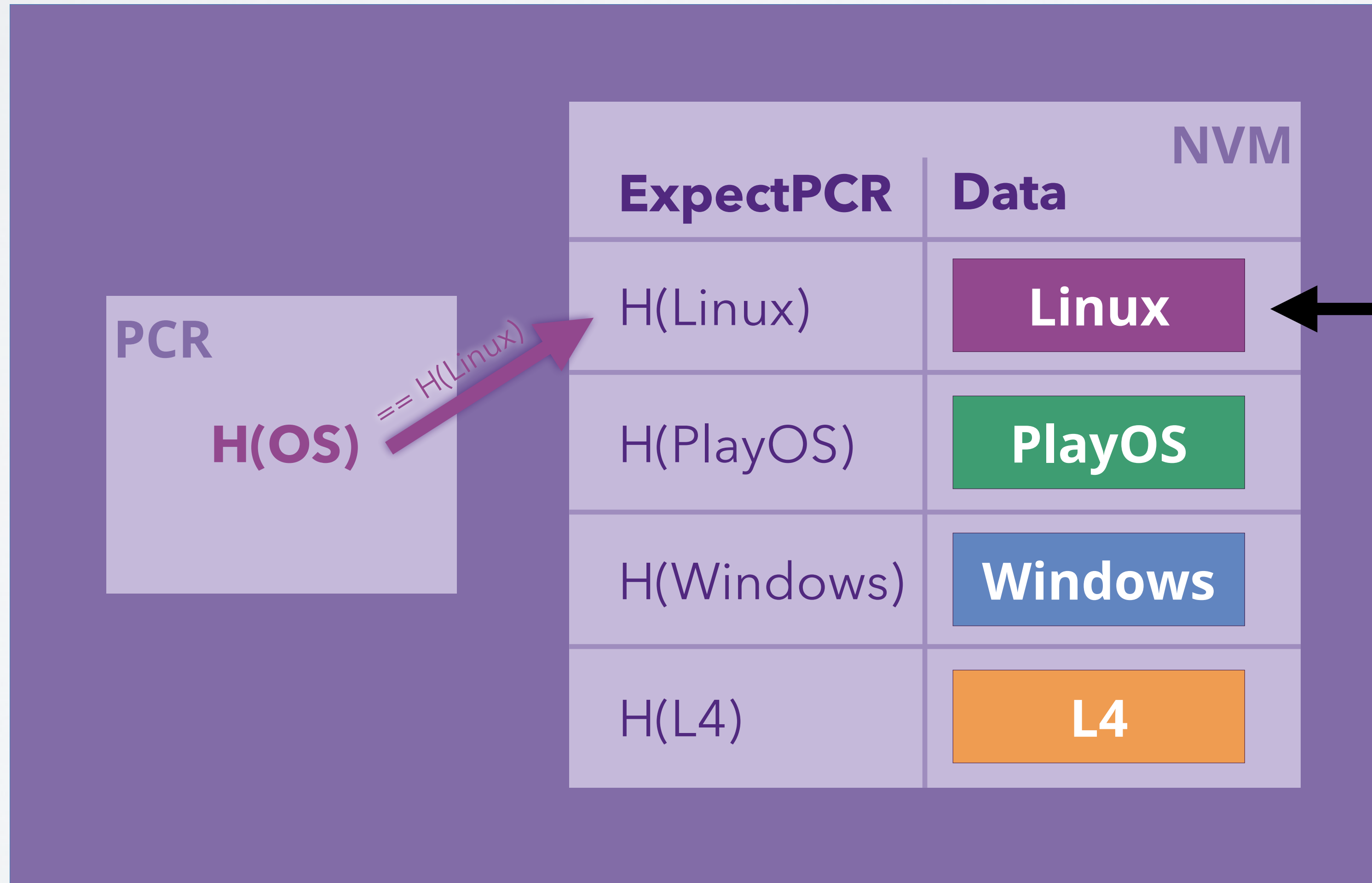
- Send data over secure channel after remote attestation
- Bind that data to software configuration via TRB

## **Problem:** How to work with this data when offline?

- Must store data for time after reboot
- For example for DRM: bind decryption key for downloaded movie to specific machine with specific OS



# Sealed Memory Principle

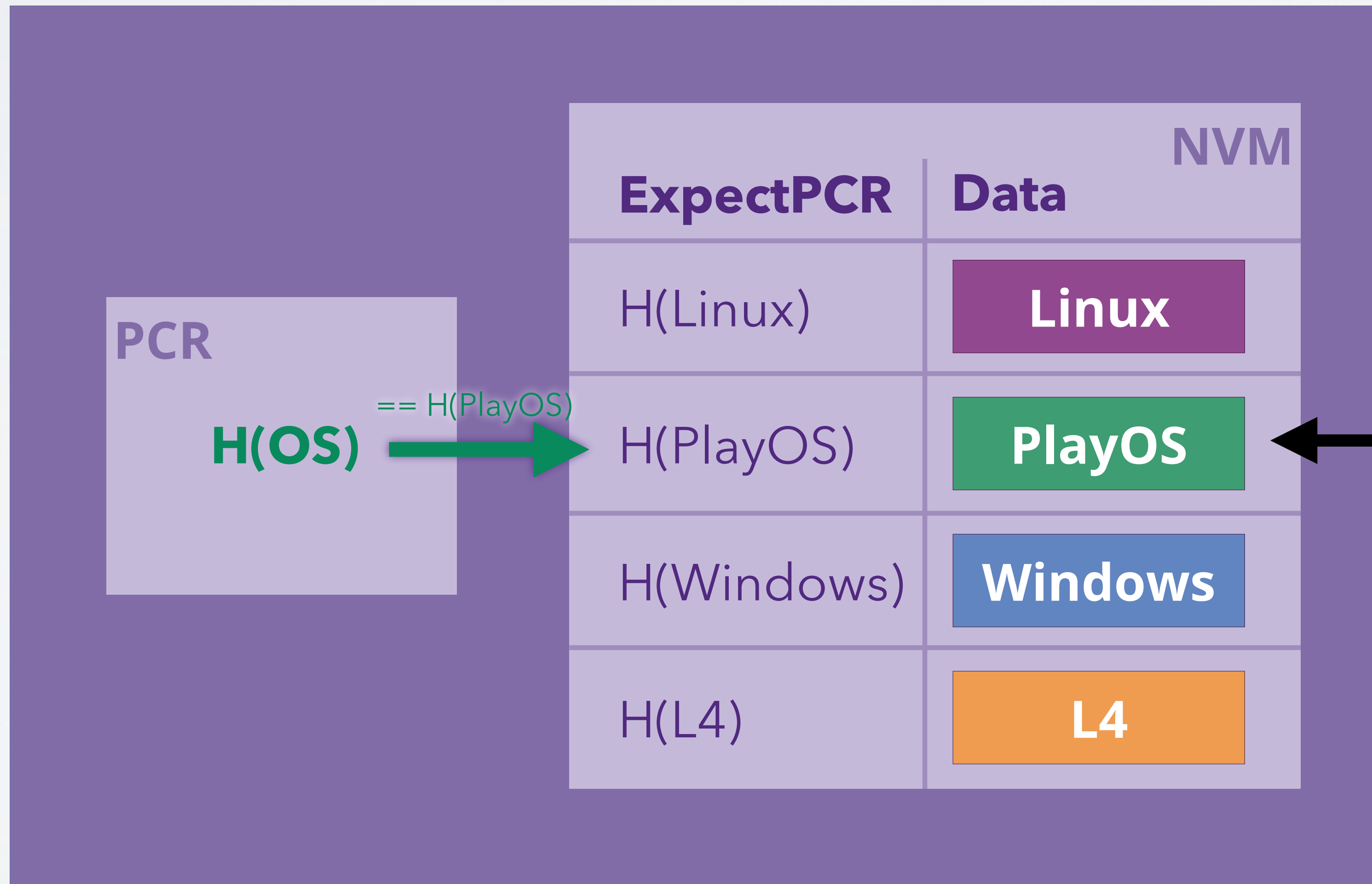


**Add/remove/read/write  
"Sealed Memory" slots**

Can be accessed by  
currently active OS

Other slots inaccessible  
due to PCR mismatch

# Sealed Memory Principle

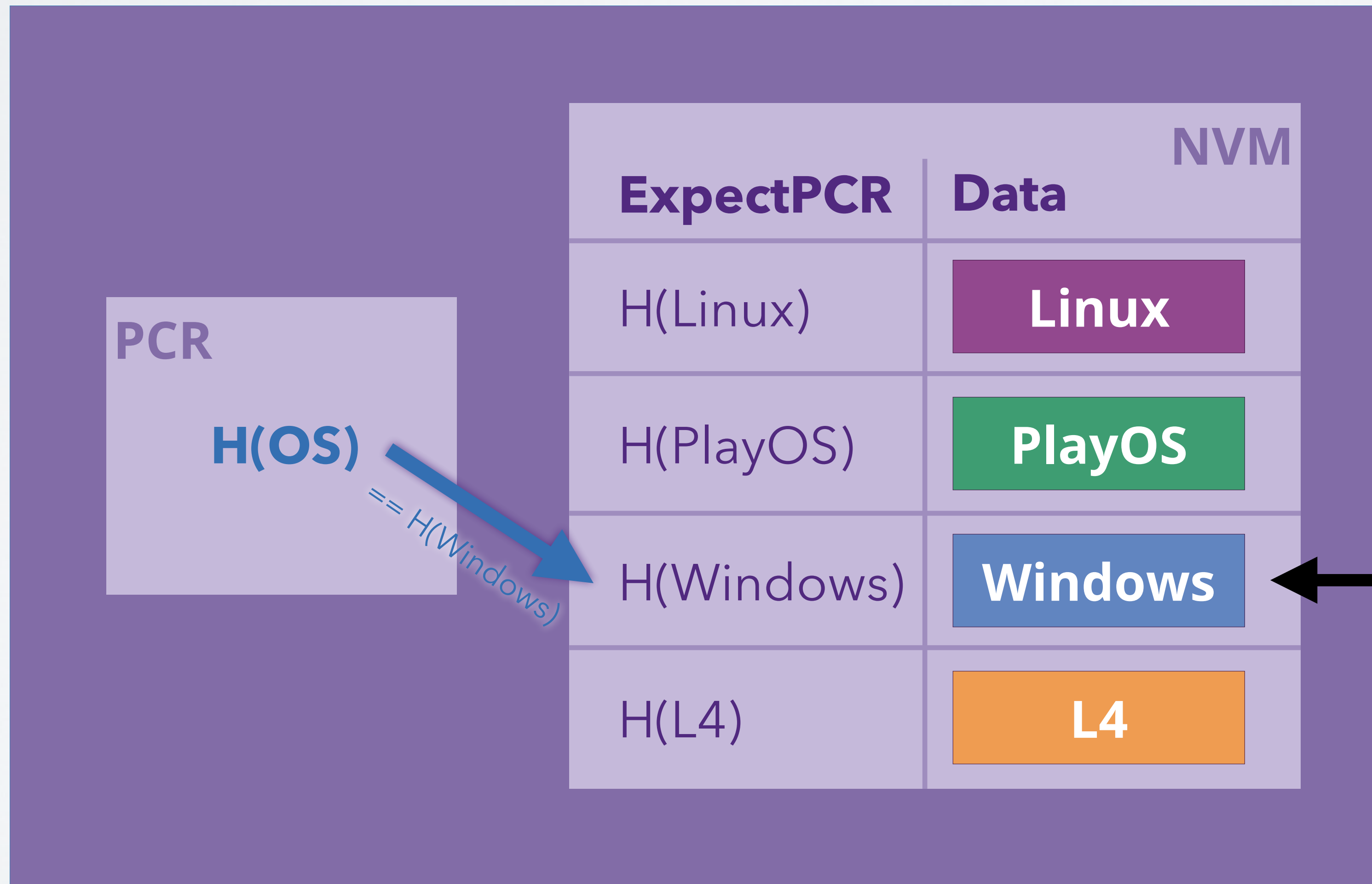


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# Sealed Memory Principle



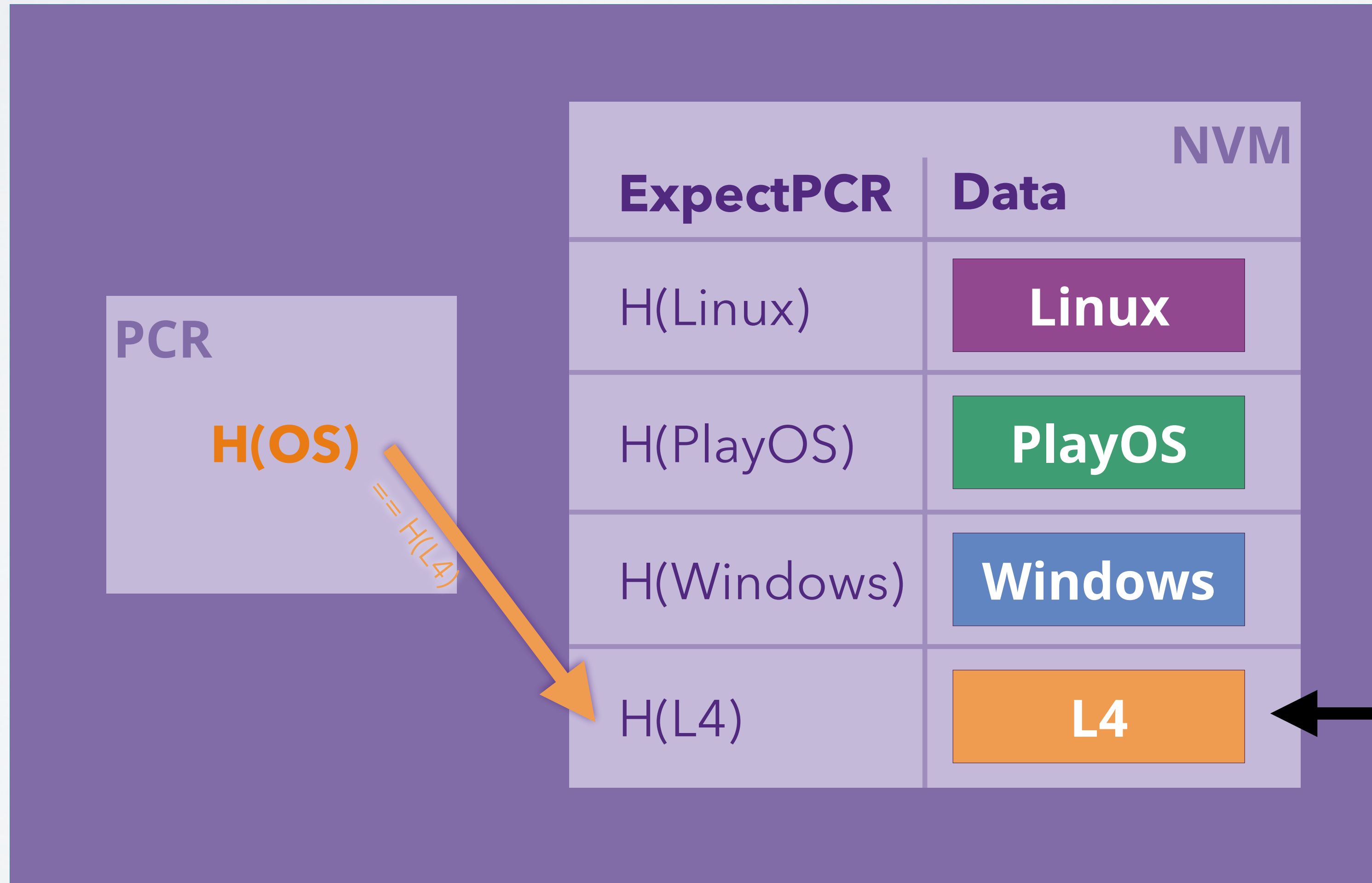
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# Sealed Memory Principle

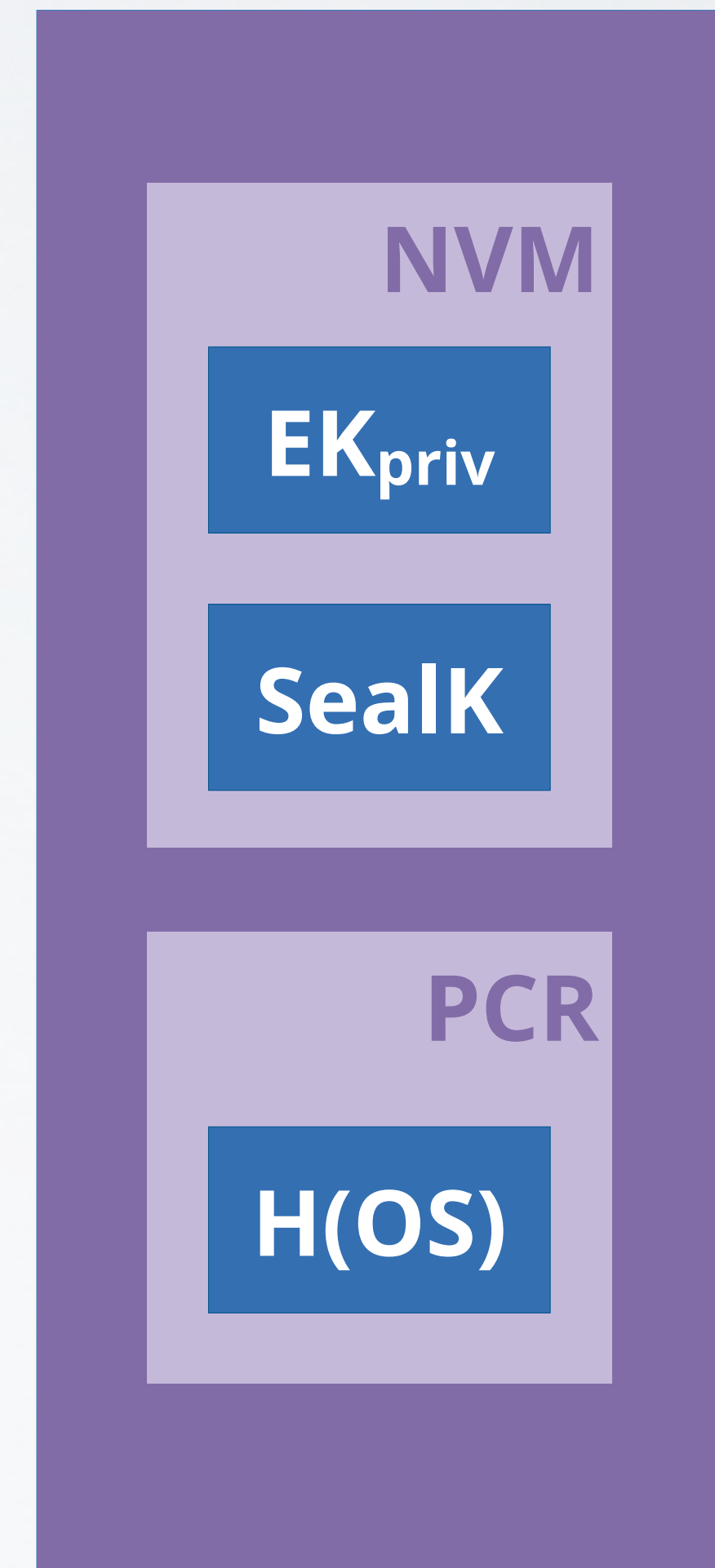


**Add/remove/read/write  
"Sealed Memory" slots**

Other slots inaccessible  
due to PCR mismatch

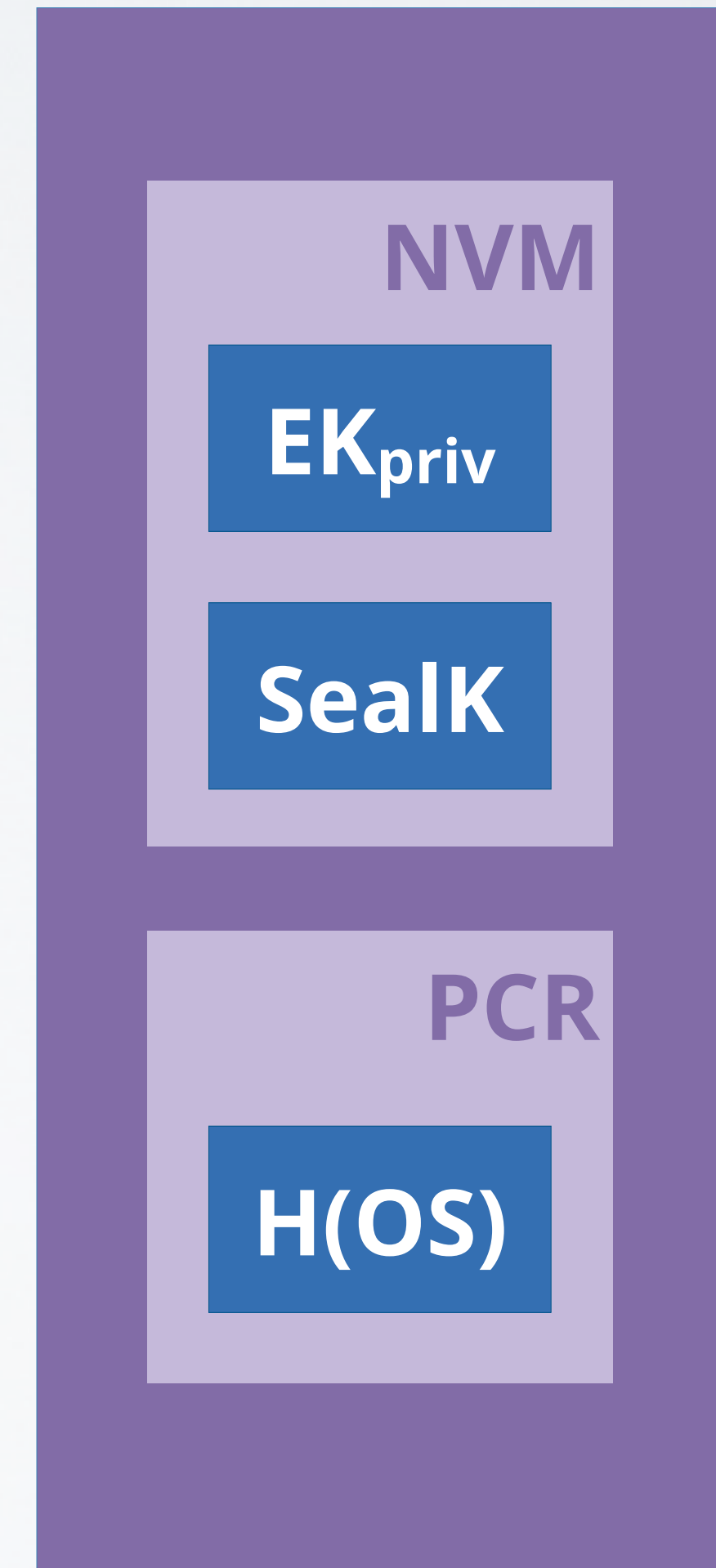
Can be accessed by  
currently active OS

- TRB creates secret symmetric key **SealK**
- TRB encrypts (**Seal**) and decrypts (**Unseal**) data using **SealK**
- **Seal(ExpectPCR, data)**  
→ **{ExpectPCR, data}SealK**
- **Unseal({ExpectPCR, data}SealK) → data**  
**iff** current **PCR == ExpectPCR**  
**else** abort without releasing data



- Sealed (encrypted) data can be stored outside of TRB, allows to keep NVM small
- When sealing, arbitrary "expected PCR" values can be specified (e.g., future version of OS, or entirely different OS)

$\{H(\text{Linux}), \text{Linux}\}\text{SealK}$        $\{H(\text{PlayOS}), \text{PlayOS}\}\text{SealK}$   
 $\{H(\text{Windows}), \text{Windows}\}\text{SealK}$        $\{H(\text{L4}), \text{L4}\}\text{SealK}$





- **Windows:**      **Seal (H(PlayOS), PlayOS\_Secret)**
  - **sealed\_message** (store it on disk)
  
- **L4:**            **Unseal (sealed\_message)**
  - PlayOS, PlayOS\_Secret
  - ExpectPCR != PlayOS
  - **abort**
  
- **PlayOS:**        **Unseal(sealed\_message)**
  - PlayOS, PlayOS\_Secret
  - ExpectPCR == PlayOS
  - **emit PlayOS\_Secret**

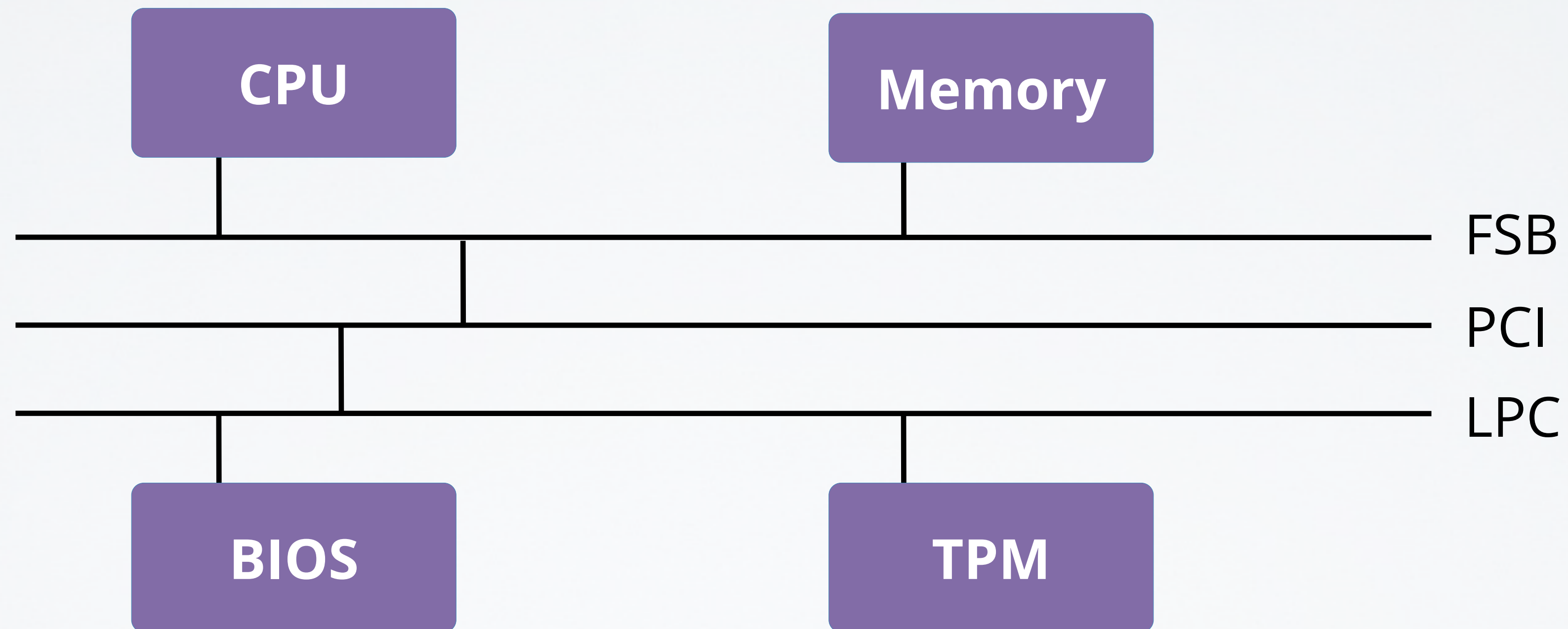
# Tamper Resistant Black Box?

**Ideally:** includes CPU, Memory, ...

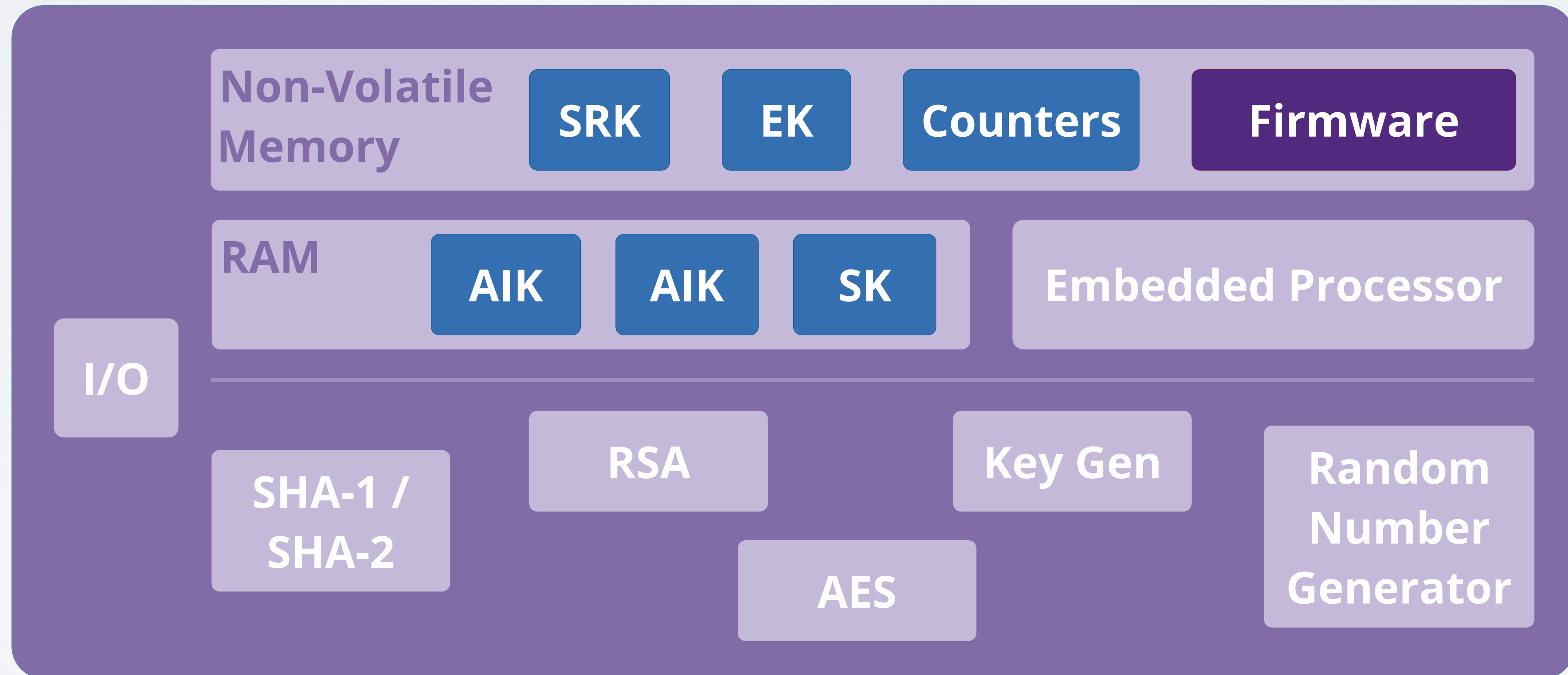
**In practice:**

- Additional physical protection (e.g., IBM 4758, → Wikipedia)
- Hardware support:
  - Trusted Platform Module (TPM): requires careful design to allow firmware updates, etc.
  - Add a new privilege mode: ARM TrustZone, Intel SGX
  - Add encrypted VMs: Intel TDX, AMD SEV, ...

# TCG PC Platform: Trusted Platform Module (TPM)







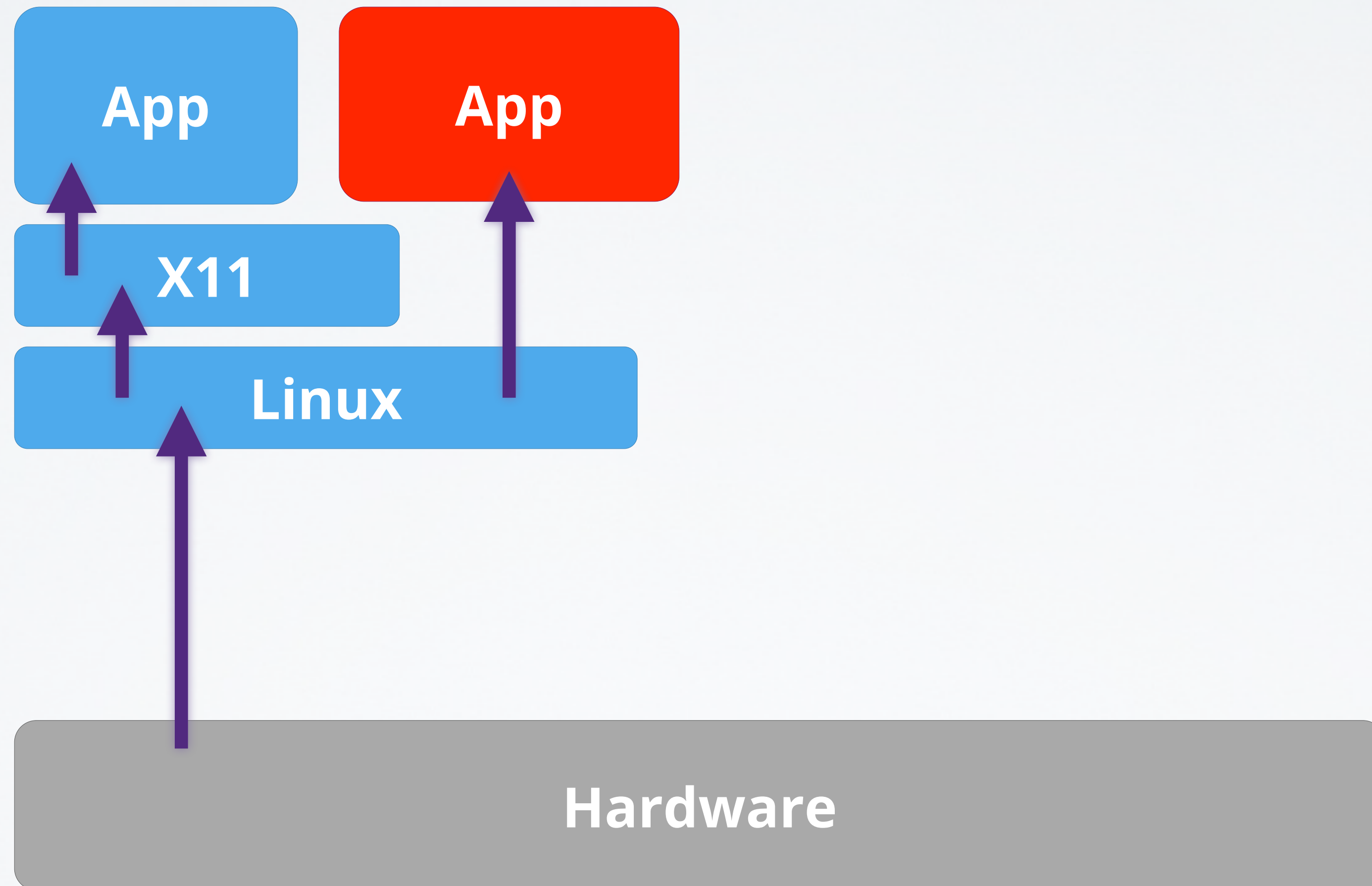
## Principle Method:

- Isolate critical software
- Rely on small Trusted Computing Base (TCB)

## Ways to implement the method:

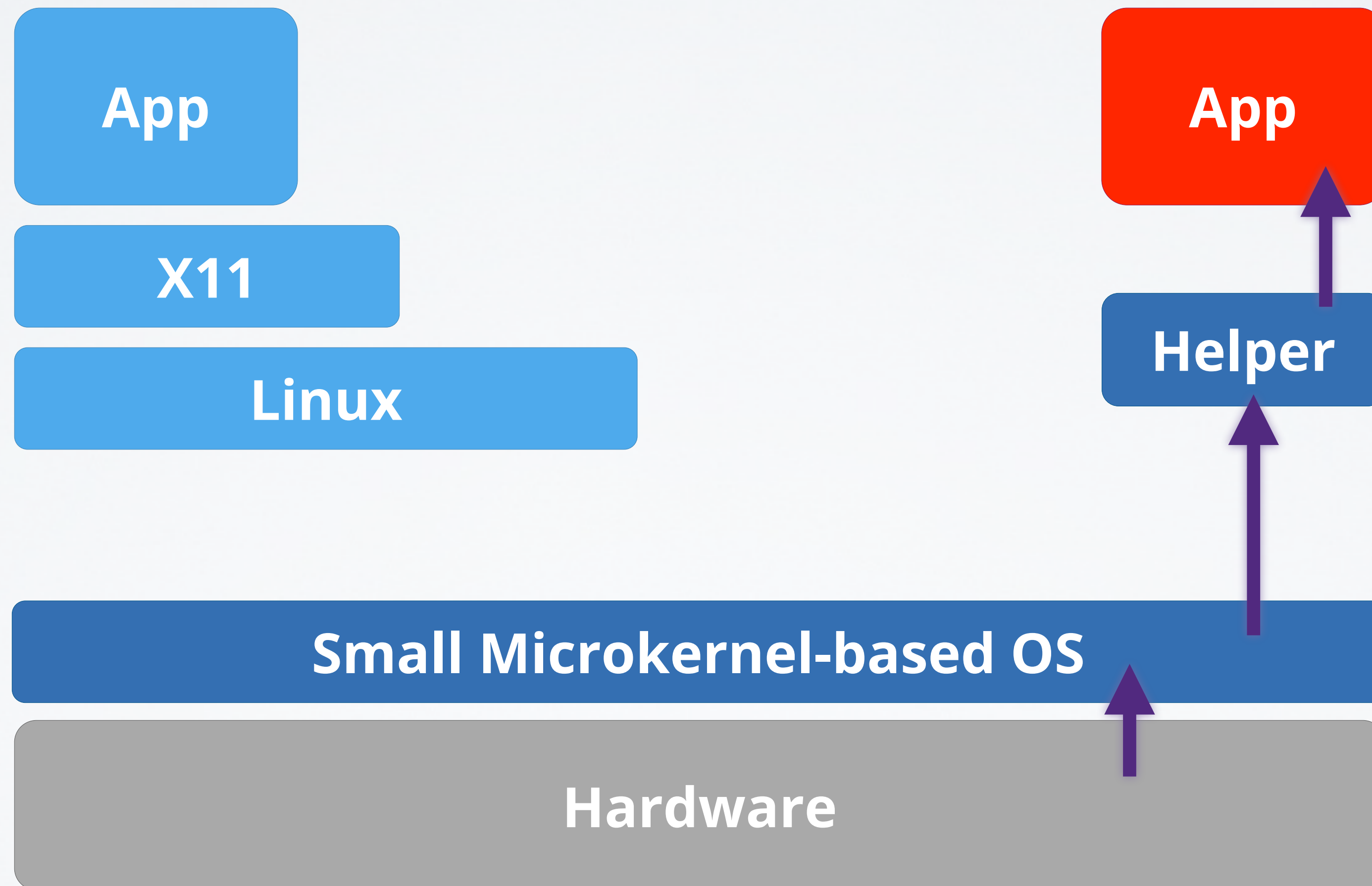
- Small OS kernels:  
microkernels, separation kernels, ...
- Hardware / microcode support

# Trusted Computing Base: Big OS

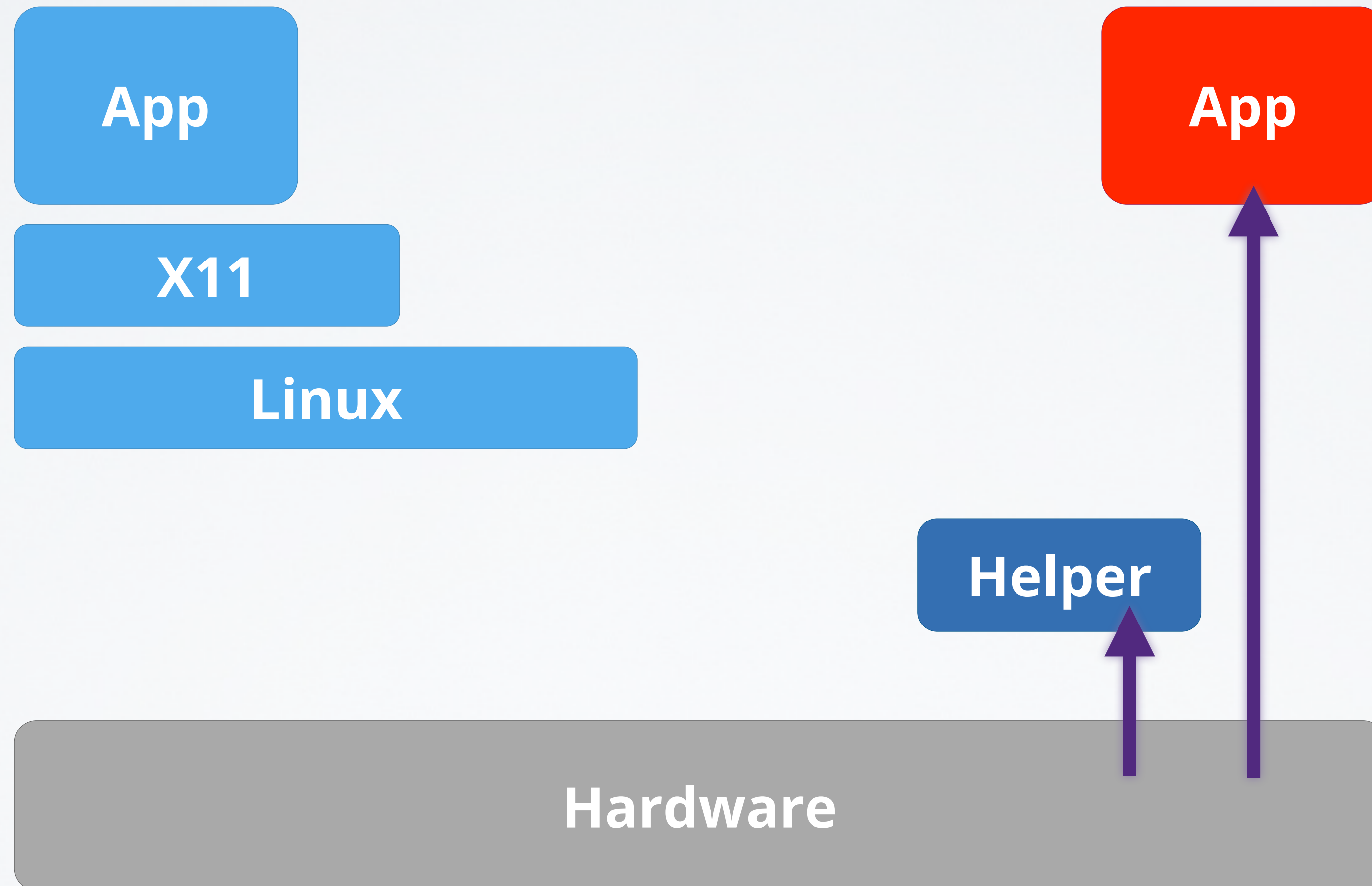


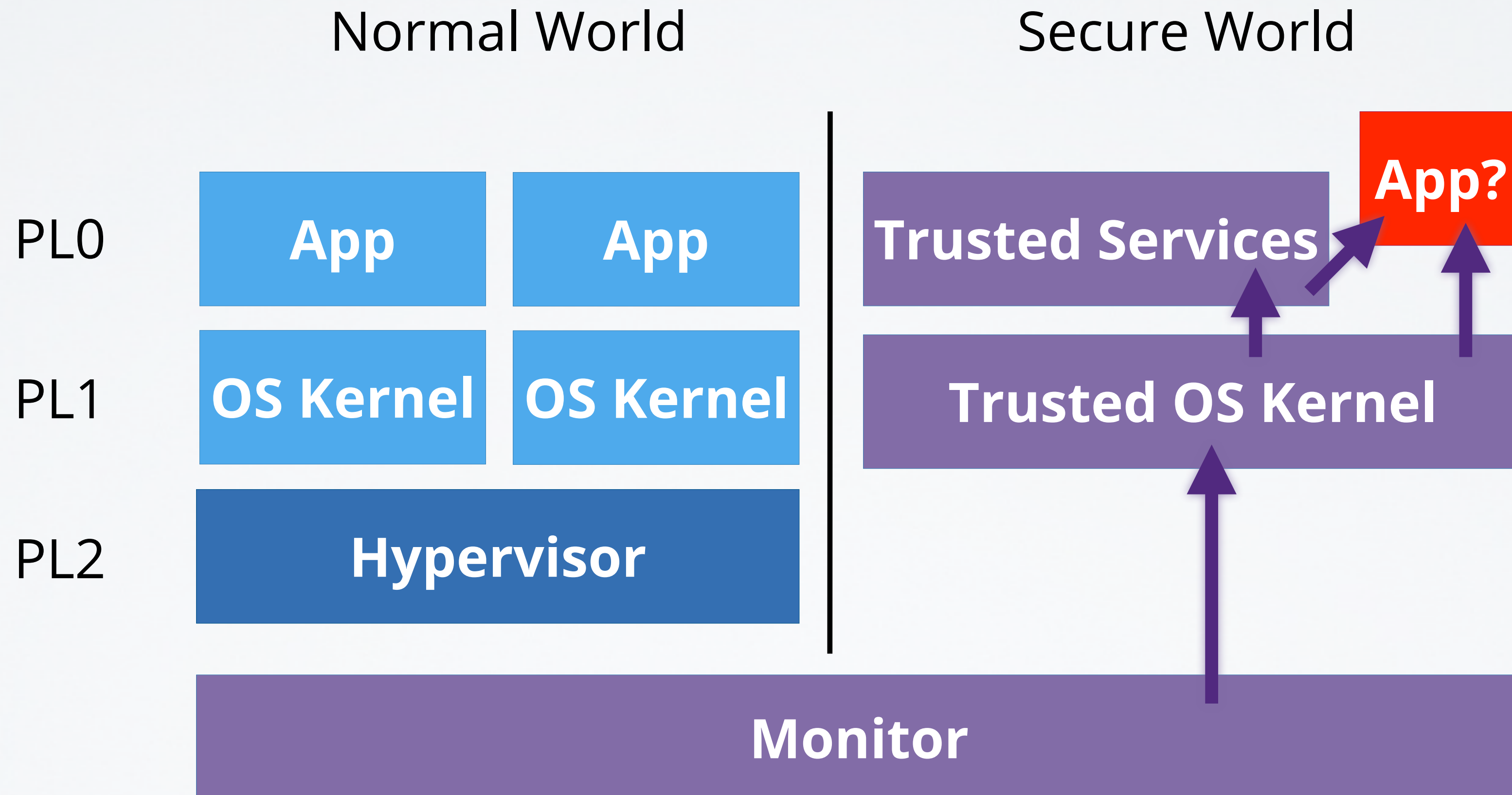


# Trusted Computing Base: Small OS

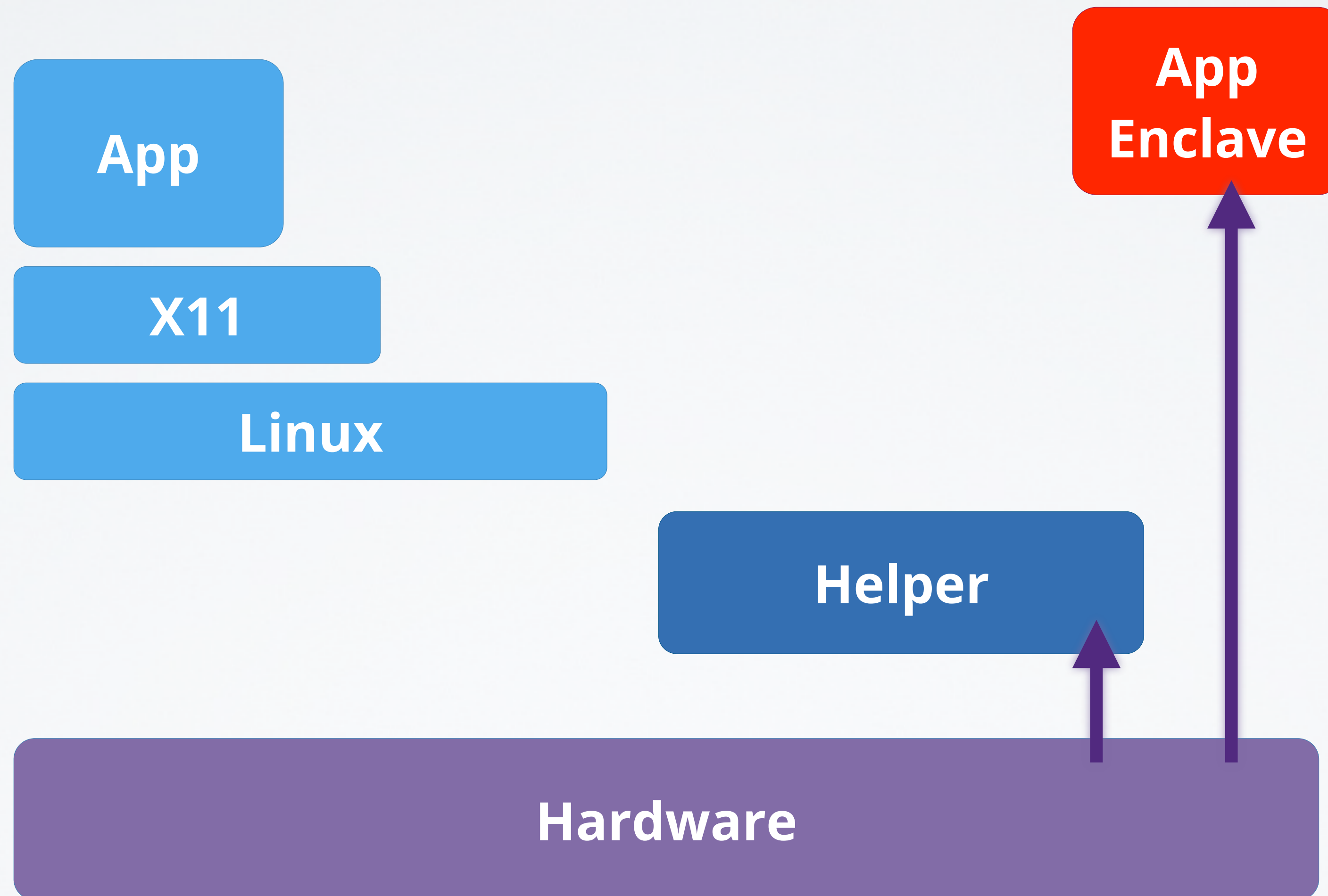


# Trusted Computing Base: Only Hardware?





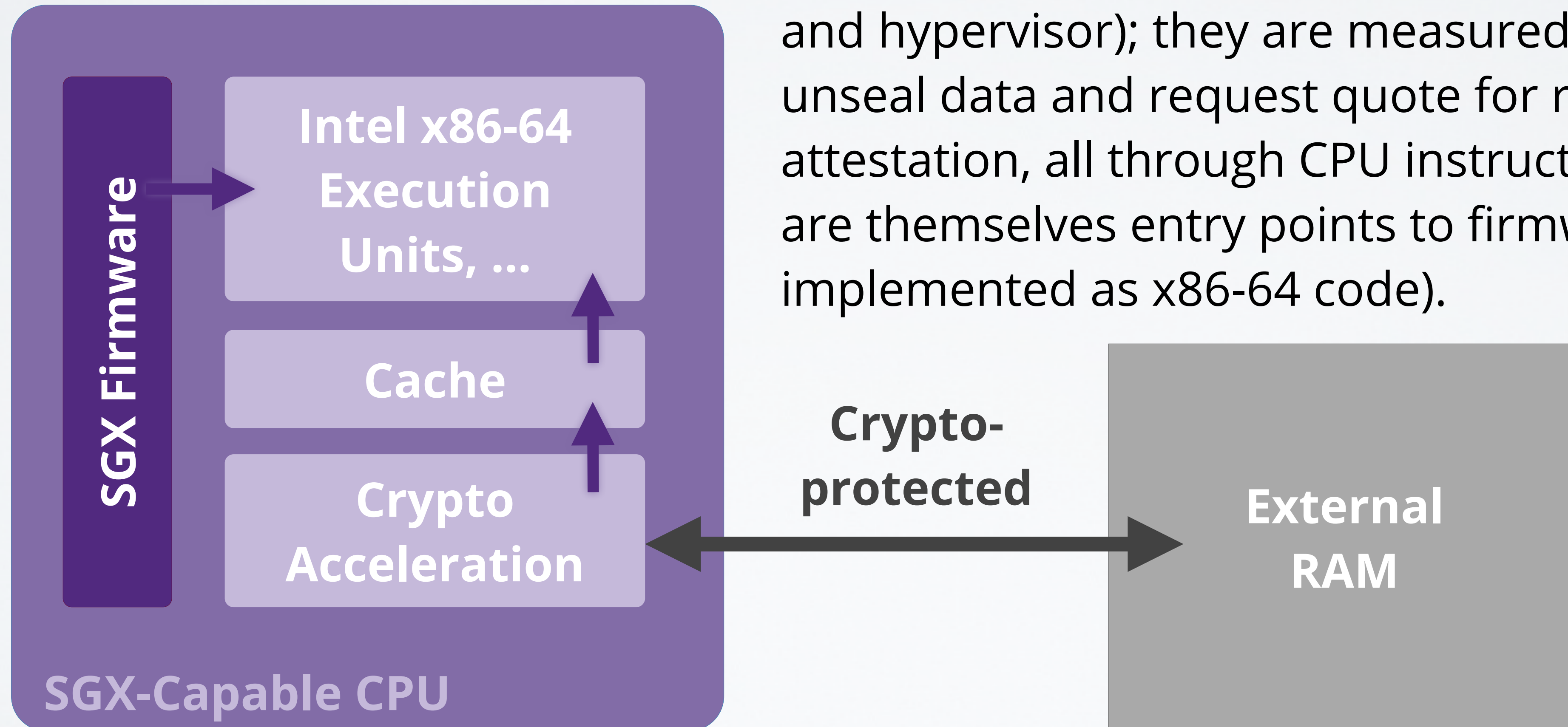




## **“Enclaves” for applications:**

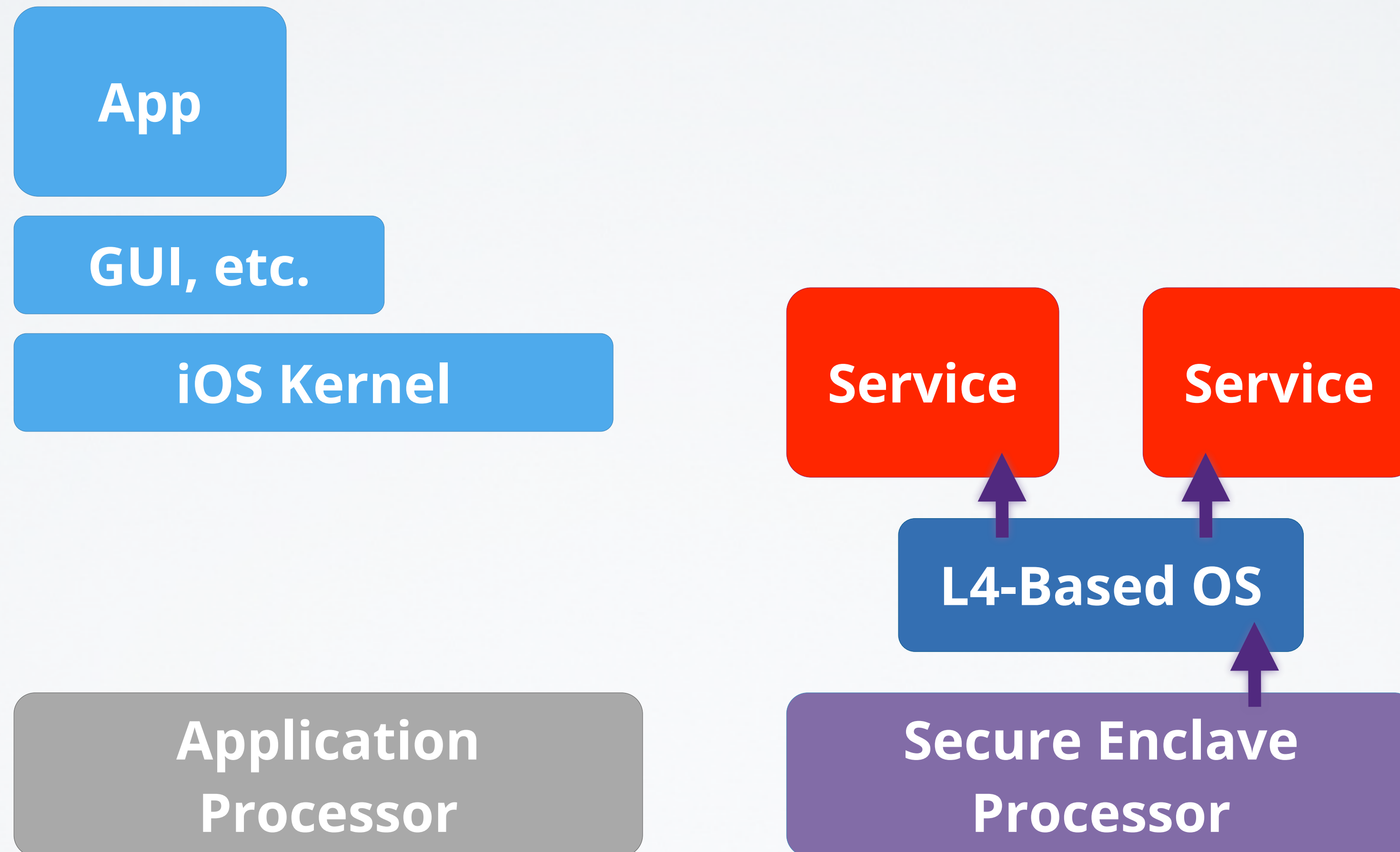
- Established per special SGX instructions
- Measured by CPU
- Provide controlled entry points
- Resource management via untrusted OS

Applications executing in enclaves benefit from hardware memory protection (also against OS and hypervisor); they are measured, seal and unseal data and request quote for remote attestation, all through CPU instructions (which are themselves entry points to firmware implemented as x86-64 code).





# Apple Secure Enclave Processor



## Important Foundational Paper:

*"Authentication in Distributed Systems: Theory and Practice"*,  
Butler Lampson, Martin Abadi, Michael Burrows, Edward  
Wobber, ACM Transactions on Computer Systems (TOCS)

## Technical documentation:

- Trusted Computing Group's specifications  
<https://www.trustedcomputinggroup.org>
- ARM Trustzone, Intel SGX vendor documentation